			- in	
	Application No.	Applicant(s)		
Notice of Allowability	09/574,647	<del></del>	VAN DOORN, SCHELTO	
	Examiner	Art Unit		
	Edwin A. León	2833		
The MAILING DATE of this communication ap All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOL-8 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	IS (OR REMAINS) CLOSED in to 35) or other appropriate communication is sub- RIGHTS. This application is sub-	his application. If not includication will be mailed in due	ed course. <b>THIS</b>	
1. X This communication is responsive to Amendment of 9/7.	704 and Interview Summary of 10	<u>0/15/04</u> .		
2. The allowed claim(s) is/are 1-10 and 20-28.				
3. $igotimes$ The drawings filed on <u>18 May 2000</u> are accepted by the	Examiner.			
4. Acknowledgment is made of a claim for foreign priority  a) All b) Some* c) None of the:  1. Certified copies of the priority documents had 2. Certified copies of the priority documents had 3. Copies of the certified copies of the priority International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DAT noted below. Failure to timely comply will result in ABANDOI THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5. A SUBSTITUTE OATH OR DECLARATION must be sull INFORMAL PATENT APPLICATION (PTO-152) which go the including changes required by the Notice of Draftsperior (a) including changes required by the Notice of Draftsperior (b) including changes required by the attached Examin Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFI each sheet. Replacement sheet(s) should be labeled as such in the deattached Examiner's comment regarding REQUIREMENT.	ave been received.  ave been received in Application documents have been received in E" of this communication to file a NMENT of this application.  bmitted. Note the attached EXANgives reason(s) why the oath or donust be submitted.  erson's Patent Drawing Review (er's Amendment / Comment or in the header according to 37 CFR posit of BIOLOGICAL MATER	No  In this national stage application of the Office action of the Claration.  RIAL must be submitted.	quirements NOTICE OF	
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-940)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/S Paper No./Mail Date	8) 6. ⊠ Interview Sun Paper No./M B/08), 7. ⊠ Examiner's A	rmal Patent Application (PT nmary (PTO-413), lail Date <u>10/15/04</u> . mendment/Comment tatement of Reasons for Allo Gary Pat Primary Ex	owence la-	

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## **DETAILED ACTION**

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### **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Brenda Binder on October 15, 2004.

The application has been amended as shown in the following attached pages:

Garý Paumen Primary Examiner

Attorney's Docket No.: 12754-064001 / 00P7629

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Schelto Van Doom Art Unit: 2833

Sexial No.: 09/574,647 Examiner: Edwin A. Leon

Filed : May 18, 2000

Title : ELECTRICALLY CONNECTING INTEGRATED CIRCUITS AND

TRANSDUCERS

### PROPOSED EXAMINER'S AMENDMENT

1. (Currently Amended) A transducer comprising:

a housing mountable mounted on a substrate, the housing configured to receive receiving a jumper cable,

an input/output (I/O) lead supported by the housing and configured to directly contact contacting an I/O lead of an integrated circuit, where the integrated circuit is separately mounted on the substrate and outside of the housing of the transducer, and

electronic circuitry supported by the housing to transition transitioning between an electronic data transfer protocol of the jumper cable and an electronic data transfer protocol of the integrated circuit.

- 2. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to electrically connect to the integrated circuit I/O lead independently of any electrically conductive path of the substrate.
- 3. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to contact the integrated circuit I/O lead at a transducer surface substantially parallel to a mounting surface of the substrate.
- 4. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to contact a pin I/O lead of the integrated circuit.
- 5. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to contact a solder ball lead of the integrated circuit.

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- 6. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to contact the integrated circuit I/O lead at a transducer surface adjacent to a mounting surface of the substrate.
- 7. (Original) The transducer of claim 1, further comprising a power input lead connectable to a power line of the substrate.
- 8. (Original) The transducer of claim 1, further comprising a transductional device.
- 9. (Original) The transducer of claim 1, wherein the transductional device is an optoelectronic device.
- 10. (Original) The transducer of claim 1, wherein the transductional device is an electronic device.
- 11 19. Cancelled.
- 20. (Currently Amended) A transducer comprising:

a housing mountable mounted on a substrate, the housing configured to receive receiving a jumper cable;

an input/output (I/O) lead supported by the housing and configured to-directly contact contacting an I/O lead of an integrated circuit, where the integrated circuit is separately mounted on the substrate and outside of the housing of the transducer; and

electronic circuitry supported by the housing to transitioning between an electronic data transfer protocol of the jumper cable and an electronic data transfer protocol of the integrated circuit, the electronic circuitry including:

a coupler operable-to couplecoupling data signals carried by a jumper cable received by the housing to a transductional device;

a transductional device operable to transition transitioning between an electronic data transfer protocol of the jumper cable and an electronic data transfer protocol of the

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integrated circuit; and

a circuit card operable to carry carrying signals between the transductional device and the I/O lead supported by the housing.

- 21. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to electrically connect to the integrated circuit I/O lead independently of any electrically conductive path of the substrate.
- 22. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact the integrated circuit I/O lead at a transducer surface substantially parallel to a mounting surface of the substrate.
- 23. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact a pin I/O lead of the integrated circuit.
- 24. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact a solder ball lead of the integrated circuit.
- 25. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact the integrated circuit I/O lead at a transducer surface adjacent to a mounting surface of the substrate.
- 26. (New) The transducer of claim 20, further comprising a power input lead connectable to a power line of the substrate.
- 27. (New) The transducer of claim 20, wherein the transductional device is an opto-electronic device.
- 28. (New) The transducer of claim 20, wherein the transductional device is an electronic device.

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# Allowable Subject Matter

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2. Claims 1-10 and 20-28 are allowed.

The references fail to teach, disclose, or suggest, either alone or in combination, the input/output (I/O) lead contacting the I/O lead of the integrated circuit, where the integrated circuit is separately mounted on the substrate and outside of the housing of the transducer and a circuit card carrying signals between the transductional device and the I/O lead supported by the housing and in combination with the rest of the limitations of the base claims.

Applicant's arguments (Pages 5-7) regarding the Curzio reference have been fully considered and are deemed persuasive.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin A. León whose telephone number is (571) 272-2008. The examiner can normally be reached on Monday - Friday 10:00-6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on 571-272-2800, extension 33. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Gary Pauman

Primary Examiner

Edwin A. Leon AU 2833

EAL

October 21, 2004